CSE 30321 - Computer Architecture I - Fall 2010

Lab 02:Programming in MIPS AssemblyTotal Points:100 pointsAssigned:September 21, 2010Due:October 7, 2010

1. <u>Goals</u>

Looking back to the course goals, this lab is directly related to Goal #4. Namely, at the end of the semester, you should be able to "explain how code written in (different) high-level languages (like C, Java, C++, Fortran, etc.) can be executed on different microprocessors (i.e. Intel, AMD, etc.) to produce the result intended by the programmer."

More specifically, the primary learning goal for this lab is to become more familiar with how computer hardware allows for the execution of recursive procedure calls.

2. Introduction and Overview

For this lab, you will be working with a recursive *mergesort* function. Mergesort will be (or may already have been) discussed in detail in your data structures class. However, a brief explanation is also included here.

Megresort can be used to sort an unordered list. The list is essentially subdivided into smaller and smaller lists, these are sorted, and the lists are then merged back together. An example is shown in Figure 1 below:



Figure 1: Graphical example of a merge sort¹.

"Mergesort can sort a file of N elements in N log N time even in the worst case. The prime <u>disadvantage</u> of *mergesort* is that extra space is required – equivalent to the size of the list (N) – to perform the sort. However, mergesort can be implemented so that it accesses data primarily in a sequential manner (i.e. one item after another). This can be <u>advantageous</u> – i.e. if sorting a linked list, where only sequential accesses are possible."²

¹ http://upload.wikimedia.org/wikipedia/commons/thumb/e/e6/Merge_sort_algorithm_diagram.svg/500px-Merge_sort_algorithm_diagram.svg.png

² Paraphrased from: "Algorithms in C" by Robert Sedgewick

In this lab, you will need to write a recursive *mergesort* function (and the corresponding *sort* function) in MIPS assembly. To help you get started, compilable C-code that successfully performs a *mergesort* of a 32 number array can be found at:

/afs/nd.edu/coursefa.10/cse/cse30321.01/Labs/02/mergesort.c

If you examine this code carefully, you will see that there is a recursive function (*mergesort*) and a nonrecursive function (*sort*). The first call to *mergesort* sorts one half of the array. The second call to *mergesort* sorts the other half of the array. The *sort* function then ensures that the elements in different array segments are merged back together in the proper order. (You will not need to write the *display* function in mergesort.c in MIPS assembly.)

Note that detailed comments and print statements have also been added to help you understand how these two functions can be combined to sort the array. (If you want to understand the functionality of merge sort better, you might direct the output of the code to a file to see – step-by-step – how the array is sorted.)

3. <u>Utilities</u>

To help you design, debug, and test your code (or any MIPS code), you should use the XSPIM utility ("SPIM" = "MIPS" backwards).

3.1 Overview

XSPIM is software that will help you to simulate the execution of MIPS assembly programs. It does a context and syntax check while loading an assembly program. In addition, it adds in necessary overhead instructions as needed, and updates register and memory content as each instruction is executed. Below, is a tutorial on how to use XSPIM.

- Go to the directory where your assembly language program is stored.
- Type /afs/nd.edu/user14/csesoft/bin/xspim at the prompt.
 - (Adding the full directory to your path, or making an alias for XSPIM will make the program easier to launch in the future.)
 - Also, note that the source for XSPIM can be found at:
 - <u>http://pages.cs.wisc.edu/~larus/spim.html</u>
 - (I have found it to be quite easy to compile and run locally.)
- A window will open as shown in Figure 2. The window is divided into five vertical sections:
 - 1. The *Register Content* section displays the content of all registers. (You only need to be concerned with the registers that we have discussed in class; ignore any others.)
 - 2. Each button in the *Command Console* corresponds to a command supported by the simulator.
 - 3. The *Text Segment* displays the MIPS instructions loaded into memory to be executed. From left to right, we have the memory addresses, the corresponding memory contents in hex, the actual MIPS instructions, and the corresponding assembly instructions.
 - 4. The *Data Segment* displays memory addresses and their values in the data and stack segments of the memory.
 - 5. The *Information Console* lists the actions performed by the simulator.

$\begin{array}{rcl} Pc &= 00400000\\ Status &= 00000000\\ R0 & (r0) &= 00000000\\ R1 & (at) &= 00000000\\ R2 & (v0) &= 00000000\\ R3 & (v1) &= 00000000\\ R4 & (a0) &= 00000000\\ R4 & (a0) &= 00000000\\ R5 & (a1) &= 00000000\\ R6 & (a2) &= 00000000\\ R7 & (a3) &= 00000000\\ FP0 &= 0.00000\\ FP0 &= 0.00000\\ FP4 &= 0.00000\\ FP6 &= 0.00000\\ FP0 &= 0.00000\\ FP0 &= 0.00000\\ \end{array}$	$\begin{array}{c} {\rm EPC} & = \\ {\rm HI} & = \\ {\rm R8} & ({\rm t0}) & = \\ {\rm R9} & ({\rm t1}) & = \\ {\rm R10} & ({\rm t2}) & = \\ {\rm R11} & ({\rm t3}) & = \\ {\rm R12} & ({\rm t4}) & = \\ {\rm R13} & ({\rm t5}) & = \\ {\rm R14} & ({\rm t6}) & = \\ {\rm R15} & ({\rm t7}) & = \\ {\rm R15} & ({\rm t7}) & = \\ {\rm R15} & ({\rm t7}) & = \\ {\rm 0} & {\rm 00} \\ {\rm FP10} & = & 0 & {\rm 00} \\ {\rm FP12} & = & 0 & {\rm 00} \\ {\rm FP12} & = & 0 & {\rm 00} \\ {\rm FP13} & = & 0 & {\rm 00} \\ {\rm S1} \\ {\rm FP8} & = & 0 & {\rm 00} \\ \end{array}$	Asprin 00000000 00000000 General Regi 00000000 00000000 R17 00000000 R18 00000000 R19 00000000 R10 00000000 R20 00000000 R21 00000000 R23 uble Floating 000 FP1 000 R2000 R21 R22 R23 R24 R25 R26 R27 R28 R29 R29 R20 R21 R25 R27 R28 R29 R29 R200 R29 R200 R29 R29 R29 R29 R29	Cause = 0000 L0 = 0000 (s1) = 00000 (s2) = 00000 (s2) = 00000 (s3) = 00000 (s5) = 00000 (s6) = 00000 Point Regist 6 = 0.00000 Point Regist 6 = 0.00000 Point Regist 6 = 0.00000	00000 B: 00000 R24 (1 0000 R25 (1 0000 R25 (1 0000 R27 (1 0000 R27 (1 0000 R29 (2 0000 R29 (2 0000 R30 (2 0000 R30 (2 FP24 : FP26 : FP28 : FP28 : FP28 : FP28 : FP28 : FP24 :	xdWAddr = 00000000 x8) = 00000000 y9) = 00000000 x0) = 00000000 x1) = 00000000 xp) = 10008000 xp) = 7fffeffc x8) = 00000000 xa) = 00000000 xa) = 00000000 xa) = 0000000 xa) = 000000 xa) = 0000000 xa) = 0000000 xa) = 0000000 xa) = 000
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[0x00400000] 07 [0x00400004] 07 [0x00400008] 07 [0x0040000c] 07 [0x00400010] 07 [0x00400014] 07 [0x00400014] 07 [0x00400016] 07 [0x00400016] 07 [0x00400016] 07 [0x00400016] 07	8 Fa40000 lw 27a50004 ad 24a60004 ad 00041080 al 000c23021 ad 00000000 ja 3402000a or 00000000c sy	12x1 32g) diu \$5, \$29, diu \$6, \$5, 4 1 \$2, \$4, 2 du \$6, \$2 1 0x00000000 i \$2, \$0, 10 scall	4 [main]	; 102: ; 103: ; 104: ; 105: ; 106: ; 107: ; 108: ; 108: ; 109:	<pre>lw \$a0, 0(\$sp) addiu \$a1, \$sp, 4 addiu \$a2, \$a1, 4 sll \$v0, \$a0, 2 addu \$a2, \$a2, \$v0 jal main li \$v0 10 syscall</pre>
Data Segments					
DATA [0x10000000][0x STACK [0x7fffeffc]	(10020000)	0x00000000			
KERNEL DAT [0x90000000] [0x90000010]	CA	0x20204578 0x63757272	0x63657074 0x65642061	0x696f6e20 0x6e642069	0x00206f63 0x676e6f72
SPIM Version 6.2 of January 11, 1999 Copyright 1990-1998 by James R. Larus (larus@cs.wisc.edu). All Rights Reserved. See the file README for a full copyright notice. Loaded: /usr/local/src/spim/bin//trap.handler					

Figure 2: XSPIM window.

- The functions of the command buttons in the *Command Console* are summarized below:
 - quit: Exit from the simulator
 - o load: Read an assembly program file into memory
 - reload: Reload the assembly program file
 - run: Execute a program to completion
 - step: Advance the execution of a program by a given step size
 - clear: Reinitialize registers or memory. (You have a choice of what to clear. To reload a program you modified, you need to clear both the registers and the memory.)
 - setvalue: Set a value in register or memory
 - o print: Print a value in register or memory
 - o breakpoint: Set or delete a breakpoint
 - help: Display the above message
- We will not use *terminal* and *mode* in this lab.

3.2 An Example Program

To help you get started with XSPIM, I've placed an example program that you can load (and immediately run) in the simulator. *It will probably be helpful for you to quickly step through this example, as it will introduce you to a few syntactical statements that you will need to use in other programs that you write (to be run in XSPIM).*

This program (as seen in Figure 3) loads 5 pieces of data in memory, adds them together, and store's the result in memory. Be sure that you understand:

- How you can load data into memory
- How you can get the starting address of a data array
- How you exit a program cleanly



Figure 3: Example program to load in XSPIM.

To run this program in XSPIM:

- 1. Download the program source from:
 - /afs/nd.edu/coursefa.10/cse/cse30321.01/Labs/02/addarray.s
- 2. Click on the "load" button and type the name of the program (i.e. addarray.s)
- 3. You can then run the program in 1 of 2 ways:
 - a. You can simply press the "run" button all instructions will be executed, and the final contents of memory and the register file will be reflected in the XSPIM window.
 - b. You can step through the program in increments from 1 to N instructions by pressing the "step" button. For example, if you set the step size to 1, a new instruction will be executed every time that you press step in the popup window. The contents of memory, the register file, etc. will change dynamically. Additionally, the instruction being executed will be highlighted in the "Text Segment" window.

Note that at the conclusion of this program, \$s1 should be equal to 0x1e (in hex) as the sum of 2_{10} , 4_{10} , 6_{10} , 8_{10} , and 10_{10} is 30_{10} (i.e. 11110_2 or $0x1e_{16}$). Also, note that when loading a program, you can type an absolute path to a .s file, but the default location is the directory that you are in when you launch XSPIM.

4. Problem

As a deliverable, you will essentially need to translate the provided C-code into MIPS assembly:

- Write the MIPS assembly for the mergesort and sort functions
- Your mergesort function must be recursive
- Mergesort should be called for the first time from main
- As is the C-code, your *mergesort* function should take as arguments the starting address of the array to be sorted, and the "endpoints" of the array segment that you wish to pass to *mergesort*.
- Your sort function will take the same arguments, but will also take the index of the "middle" element as an argument.
- Note: because of the way that XSPIM manages memory, you will also want to pass in the starting address of your "scratch space" (i.e. the address of the array *b* in the corresponding C-code).

Note that to simplify the code that you need to write, you can assume that the size of your array is a power of 2. (A real *mergesort* would need to handle the boundary cases in the merge loop that would make this lab much more complex.)

A skeleton has been provided at: /afs/nd.edu/coursefa.10/cse/cse30321.01/Labs/02/lab02.s

- Note that this skeleton includes the list of 16 numbers that you will need to sort.
- \circ The scratch array *b* has also been initialized such that every element is equal to 0.

5. <u>What to Turn In</u>

- Put your code for lab02.s in the dropbox of one group member. (Create a folder called Lab02)
 - Be sure that your code runs in XSPIM as we will use it to test your code with a different array.
- In the same directory, create a text file that lists the total number of instructions executed for your program.
- There is no formal report required for this lab.