

**EE 20242**

**Electronics I**

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**Homework #9**

***Due 4/16/14***

Problem 1: Sedra & Smith, problem 5.9

Problem 2: Sedra & Smith, problem 5.17

Problem 3: Sedra & Smith, problem 5.38

Problem 4: Sedra & Smith, problem 5.41

Problem 5: Sedra & Smith, problem 5.48. Note that “edge of saturation” means the boundary between saturation and triode mode operation.

Problem 6: Sedra & Smith, problem 5.49

Problem 7: Sedra & Smith, problem 5.55