Chapter 5

Transformers

5.1 Model

In this section, we define transformers and relevant variants, and how transformers are used to describe formal languages. This section is adapted from Section 4 of the survey by Strobl et al. (2024).

Transformers are composed of an input layer (Section 5.1.1), one or more hidden layers (Section 5.1.4), and an output layer. The inputs and outputs of the layers are sequences of vectors, which we treat as members of $(\mathbb{R}^d)^*$.

5.1.1 Input layer

Strings are initially mapped to sequences of vectors by $emb: \Sigma^* \xrightarrow{lp} (\mathbb{R}^d)^*$, which is the sum of a *word embedding* WE: $\Sigma \to \mathbb{R}^d$ and a *position(al) embedding* or *encoding* PE_n: $[n] \to \mathbb{R}^d$ for $n \in \mathbb{N}_{>0}$:

$$emb(w_0 \cdots w_{n-1})[i] = WE(w_i) + PE_n(i).$$
(5.1)

In theoretical constructions, the word embedding can be any computable function.

The original transformer paper (Vaswani et al., 2017) introduced the following position embedding:

$$PE_n(i)[j] = \begin{cases} \sin(10000^{-j/d} \cdot i) & \text{if } j \text{ even} \\ \cos(10000^{-(j-1)/d} \cdot i) & \text{if } j \text{ odd.} \end{cases}$$
(5.2)

Theoretical papers have explored other position embeddings, including *i* itself (Pérez, Barceló, et al., 2021), i/n (Yao et al., 2021; Chiang and Cholak, 2022), and 1/i or $1/i^2$ (Pérez, Barceló, et al., 2021).

5.1.2 Attention

Scaled dot-product self-attention with *d* input/output dimensions and d_{hid} key/value dimensions is a function

$$att: (\mathbb{R}^{d})^{*} \xrightarrow{\mathrm{lp}} (\mathbb{R}^{d})^{*}$$

$$X \mapsto Y \tag{5.3}$$

$$O[i] = W^{\mathbb{Q}}(X[i]) \tag{5.4}$$

$$\mathbf{K}[i] = \mathbf{W}^{\mathrm{K}}(\mathbf{X}[i]) \tag{5.5}$$

$$\mathbf{X}[j] = \mathbf{W}^{\mathsf{V}}(\mathbf{X}[j]) \tag{5.5}$$
$$\mathbf{V}[i] = \mathbf{W}^{\mathsf{V}}(\mathbf{X}[i]) \tag{5.6}$$

$$\mathbf{s}[i,j] = \frac{\mathbf{Q}[i] \cdot \mathbf{K}[j]}{\sqrt{d_{i+1}}}$$
(5.7)

$$\alpha[i,:] = \operatorname{softmax}(\mathbf{s}[i,:])$$
(5.8)

$$= \frac{\exp \mathbf{s}[i,:]}{\sum \exp \mathbf{s}[i,j]}$$
(5.9)

$$\mathbf{Y}[i] = \sum_{j \in [n]} \alpha[i, j] \mathbf{V}[j]$$
(5.10)

with parameters

$$\mathbf{W}^{\mathrm{Q}}, \mathbf{W}^{\mathrm{K}} \in \mathbb{R}^{d_{\mathrm{hid}} imes d}$$

 $\mathbf{W}^{\mathrm{V}} \in \mathbb{R}^{d imes d}$

We call the Q[i] the *queries*, the K[j] the *keys*, the V[j] the *values*, the s[i, j] the *attention scores*, and we call the $\alpha[i, j]$ the *attention weights*.

Real transformers use *multi-head* self-attention, but we don't use it because it can be emulated using single-head self-attention.

Attention masking In *future-masked* (also known as *causally*-masked) self attention, a term m(i, j) is added to Eq. (5.7) to force every position to attend only to preceding positions:

$$m(i,j) = \begin{cases} 0 & \text{if } j \le i \\ -\infty & \text{otherwise.} \end{cases}$$
(5.11)

(We define $\exp(-\infty) = 0$.) Some papers use *strict* future-masking, that is, m(i, j) = 0 iff j < i, and occasionally *past*-masking $(j \ge i)$ and strict past-masking (j > i).

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Hard attention Some theoretical analyses simplify attention by replacing the softmax with variants that focus attention only on the position(s) with the maximum value, breaking ties in various ways.

For any vector $\mathbf{x} \in \mathbb{R}^d$, define $M(\mathbf{x}) = \{i \in [n] \mid \forall j \in [n], \mathbf{x}[j] \leq \mathbf{x}[i]\}$ to be the set of indices of the maximal elements of \mathbf{x} . In *leftmost*-hard attention, the leftmost maximal element is used, replacing Eq. (5.8) with:

$$\alpha[i, j] = \mathbb{I}[j = \min M(\mathbf{s}[i, :])].$$
(5.12)

whereas in average-hard attention, the maximal elements share weight equally:

$$\alpha[i,j] = \frac{\mathbb{I}[j \in M(\mathbf{s}[i,:])]}{|M(\mathbf{s}[i,:])|}.$$
(5.13)

Leftmost-hard attention was previously called *hard* attention by Hahn (2020) and *unique-hard* attention by Hao et al. (2022). One may also consider rightmost-hard attention, in which the rightmost maximal element is used. Average-hard attention was also called *hard* attention by Pérez, Barceló, et al. (2021) and *saturated* attention by Merrill, Sabharwal, et al. (2022), and has been argued to be a realistic approximation to how trained transformers behave in practice (Merrill, Ramanujan, et al., 2021). Neither type of hard attention should be confused with the concept of hard attention used in computer vision (e.g., Xu et al., 2015).

5.1.3 Layer normalization

A *d*-dimensional *layer normalization* (Ba et al., 2016), or *layernorm* for short, is a function

norm:
$$\mathbb{R}^{d} \to \mathbb{R}^{d}$$

 $\mathbf{x} \mapsto \gamma \odot \frac{\mathbf{x} - \bar{\mathbf{x}}}{\sqrt{\operatorname{Var}(\mathbf{x}) + \varepsilon}} + \beta$ (5.14)

where \odot is component-wise multiplication,

$$\bar{\mathbf{x}} = \frac{1}{d} \sum_{i \in [d]} \mathbf{x}_i \tag{5.15}$$

$$\operatorname{Var}(\mathbf{x}) = \frac{1}{d} \sum_{i \in [d]} (\mathbf{x}_i - \bar{\mathbf{x}})^2$$
(5.16)

and the parameters are

$$\gamma, \beta \in \mathbb{R}^d$$
$$\varepsilon \ge 0.$$

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The original definition of layernorm (Ba et al., 2016) sets $\varepsilon = 0$, but, for numerical stability, and to avoid division by zero, all implementations we are aware of set $\varepsilon > 0$. Observe that *norm* is Lipschitz-continuous iff $\varepsilon > 0$.

Some transformer analyses omit layernorm for simplicity (e.g. Pérez, Barceló, et al., 2021).

5.1.4 Hidden layers

A *transformer layer* (also known as a *block*) comes in two variants. The *post-norm* variant (Vaswani et al., 2017) is

$$layer: (\mathbb{R}^{d})^{*} \xrightarrow{\mathbb{P}} (\mathbb{R}^{d})^{*}$$

$$X \mapsto Y \text{ where}$$

$$H = norm_{1}(X + att(X))$$

$$Y = norm_{2}(H + ffn(H))$$
(5.17)

and the pre-norm variant (Wang et al., 2019) has

$$H = X + att(norm_1(X))$$

$$Y = H + ffn(norm_2(H))$$
(5.18)

where

- *att* is a self-attention with *d* input/output dimensions and d_{hid} key/value dimensions
- *ffn* is a feed-forward network with *d* input/output dimensions and two layers, one ReLU and one linear. In applications, the hidden layer usually has 4*d* dimensions, but theoretical constructions use as many or as few dimensions as needed.
- *norm*₁ and *norm*₂ are layernorms with *d* dimensions.

In both variants, the X + and H + terms are called *residual connections* (He et al., 2015), also known as *skip connections*.

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5.1.5 Transformer encoders

A post-norm transformer encoder is a length-preserving function

$$tfr: \Sigma^{*} \xrightarrow{i_{p}} (\mathbb{R}^{d})^{*}$$

$$\mathbf{w} \mapsto \mathbf{A}^{(L)} \text{ where}$$

$$\mathbf{A}^{(0)} = emb(w)$$

$$\mathbf{A}^{(1)} = layer_{1}(\mathbf{A}^{(0)})$$

$$\vdots$$

$$\mathbf{A}^{(L)} = layer_{L}(\mathbf{A}^{(L-1)})$$
(5.19)

where

- *emb* is an input layer
- *L* is called the *depth*
- each $layer_{\ell}$ for $\ell \in [L]$ is a post-norm transformer layer (5.17).

A *pre-norm* transformer encoder is additionally parameterized by the weights of a final layernorm *norm* and is defined as:

$$tfr: \Sigma^* \xrightarrow{h_p} (\mathbb{R}^d)^*$$

$$\mathbf{w} \mapsto norm(\mathbf{A}^{(L)}) \text{ where}$$

$$\mathbf{A}^{(0)} = emb(\mathbf{w})$$

$$\mathbf{A}^{(1)} = layer_1(\mathbf{A}^{(0)})$$

$$\vdots$$

$$\mathbf{A}^{(L)} = layer_L(\mathbf{A}^{(L-1)})$$
(5.20)

where

- *emb* is an input layer
- *L* is called the *depth*
- each $layer_{\ell}$ for $\ell \in 1, ..., L$ is a pre-norm transformer layer (5.18)
- norm is a layernorm.

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The encoder's output is a sequence of vectors in $(\mathbb{R}^d)^*$. To use it as a language recognizer, we add a linear output layer

$$out: \mathbb{R}^d \to \mathbb{R}$$
$$\mathbf{h} \mapsto \mathbf{W}\mathbf{h} + b \qquad i \in [n]$$
(5.21)

with parameters $\mathbf{W} \in \mathbb{R}^{1 \times d}$ and $b \in \mathbb{R}$. The encoder accepts iff $out(tfr(w)[n]) \ge 0$.

Exercise 5.1. One very common operation in a transformer is for each position to look at the position to its immediate left. (Let's say the leftmost position looks at itself.) Assume we have a sequence of vectors

$$\mathbf{X} = \begin{bmatrix} x_0 \\ 1 \\ 0 \\ 0 \\ 0 \end{bmatrix}, \begin{bmatrix} x_1 \\ 1 \\ 1 \\ 1 \\ 0 \end{bmatrix}, \begin{bmatrix} x_2 \\ 1 \\ 2 \\ 4 \\ 0 \end{bmatrix}, \dots, \begin{bmatrix} x_i \\ 1 \\ i^2 \\ 0 \end{bmatrix}, \dots, \begin{bmatrix} x_{n-1} \\ 1 \\ n-1 \\ (n-1)^2 \\ 0 \end{bmatrix},$$

for some sequence $x_0, \ldots, x_{n-1} \in \mathbb{R}$. Write down an *average*-hard attention transformer layer *f* that computes

$$f(\mathbf{X}) = \begin{bmatrix} x_0 \\ 1 \\ 0 \\ 0 \\ x_0 \end{bmatrix}, \begin{bmatrix} x_1 \\ 1 \\ 1 \\ 1 \\ x_0 \end{bmatrix}, \begin{bmatrix} x_2 \\ 1 \\ 2 \\ 4 \\ x_1 \end{bmatrix}, \dots, \begin{bmatrix} x_i \\ 1 \\ i \\ i^2 \\ x_{i-1} \end{bmatrix}, \dots, \begin{bmatrix} x_{n-1} \\ 1 \\ n-1 \\ (n-1)^2 \\ x_{n-2} \end{bmatrix}$$

Hint: The function $2j - j^2$ is maximized when j = 1. Can you tweak this function to be maximized at j = i - 1, and can you construct the f so that $\mathbf{s}[i, j]$ equals this function?

5.2 Expressivity

The two seminal papers in the area of expressivity of transformers are often summarized as follows:

Transformers are Turing-complete (Pérez, Marinković, et al., 2019; Pérez, Barceló, et al., 2021).

Transformers, given a string of 0's and 1's, cannot tell whether the number of 1's is odd or even (Hahn, 2020).

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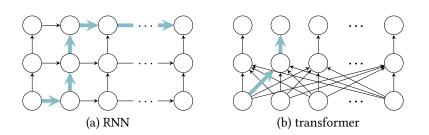


Figure 5.1: Thought of as computation graphs, an RNN (a) has a path of length O(n), but a transformer (b) only has paths of length O(1).

How can both of these statements be true? They rely on different assumptions about what a transformer is and what it means for a transformer to recognize a formal language. One of our goals in the next three chapters is to disentangle these assumptions and help you to understand how all the results in this area of research fit together.

5.2.1 Parallelism

In *Theory of Computing*, you learned about the Chomsky hierarchy of regular languages, context-free languages, context-sensitive languages, and Turing-recognizable (or recursively enumerable) languages, and in the previous chapter, we saw how RNNs could be related to two levels of that hierarchy (regular and Turing-recognizable).

With transformers, we will see that under the right assumptions, they, too, can be related to Turing machines. However, none of the other levels of the Chomsky hierarchy seem to be a good fit for transformers. In particular, there are non-regular languages that transformers can express and some regular languages that transformers apparently can't express.

In a word, the reason is *parallelism*. If you think of a neural network as a computation graph, then an RNN has a path of length n (Fig. 5.1a). So does a finite automaton. But a transformer only has paths of bounded length (Fig. 5.1b).

One reason for the difference is trainability: long paths can lead to vanishing or exploding gradients. Another reason, and the reason given in the original transformer paper (Vaswani et al., 2017), is that networks with short paths are more parallelizable on GPUs.

5.2.2 Circuit complexity

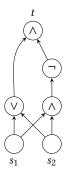
In complexity theory, one way of modeling parallelizability is *circuit complexity*. This section, which is an expanded version of Section 5.2 of the survey by Strobl

et al. (2024), gives a brief introduction to circuit complexity. For a more detailed treatment, please see the textbook by Arora and Barak (2009).

In this section, we deal a lot with bits. We write $\log n$ for $\lceil \log_2 n \rceil$, which is the number of bits needed to represent a number in $\lfloor n \rfloor$.

Circuits operate on binary values, so for the rest of this section, we assume $\Sigma = \{0, 1\}$. (If we want to use circuits to model sets of strings over an alphabet Σ , we can choose a fixed-length encoding of the symbols of Σ as strings of $b = \log |\Sigma|$ bits and encode the value of the *i*-th input symbol into positions *ib* to ib + (b-1).)

Example 5.2. Here's a circuit with input length 2. It computes the XOR function. We draw the inputs at the bottom and the output at the top.

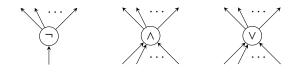


Definition 5.3 (Boolean circuits). A *(Boolean) circuit C* with input length *n* is a directed acyclic graph with

1. *n* nodes s_0, \ldots, s_{n-1} with zero fan-in, designated as *input* nodes:



2. zero or more fan-in (in-degree) gate nodes, each labeled with a function:



- NOT (\neg) , with fan-in one.
- AND (∧), with arbitrary fan-in. (An AND gate with fan-in zero always has value 1.)

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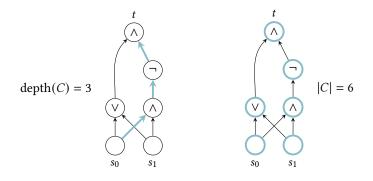
- OR (∨), with arbitrary fan-in. (An OR gate with fan-in zero always has value 0.)
- 3. A node *t*, which can be either an input or gate node, is designated the *output* of the circuit.



Given an input string $\mathbf{w} \in \{0, 1\}^n$, each input node s_i is assigned the value w_i , and each gate node labeled f computes its value by applying f to the values of its in-neighbors. Thus, we can think of the circuit as computing a function $C: \{0, 1\}^* \rightarrow \{0, 1\}$, mapping each input string \mathbf{w} to the value of t.

The *depth* of *C*, denoted depth(*C*), is the length of the longest directed path from any s_i to *t*. The *size* of *C*, denoted |C|, is the number of nodes in *C*.

Example 5.4. The longest path in *C* in Example 5.2 is 3, so depth(*C*) = 3. The number of nodes in *C* is 6, so |C| = 6.



Definition 5.5 (Boolean circuit families). A *circuit family* is a sequence $C = (C_n)_{n \in \mathbb{N}}$ such that for each *n*, C_n is a circuit with input length *n*. We treat *C* as a function on $\{0, 1\}^*$ as follows.

For every $\mathbf{w} \in \{0, 1\}^*$ with length n, $C(\mathbf{w}) = C_n(\mathbf{w})$. Then the language defined by C is $L(C) = \{\mathbf{w} \in \{0, 1\}^* \mid C(\mathbf{w}) = 1\}$. The *depth* and *size* of C are the functions $n \mapsto \text{depth}(C_n)$ and $n \mapsto |C_n|$.

Since the depth and size of a circuit family are functions, we are interested in how they depend asymptotically on *n*. In particular, since transformers have constant depth, circuit classes with constant depth are of particular interest.

Definition 5.6 (AC^k, TC^k, and NC^k). We define the following classes of languages:

- AC^k is the class of languages that can be recognized by families of circuits with unbounded fan-in, O(poly(n)) size, and O((log n)^k) depth.
- TC^k is like AC^k, but also allows MAJORITY gates, which have unbounded fan-in and output 1 iff at least half of their inputs are 1.
- NC^k is the class of languages that can be recognized by families of circuits with fan-in at most 2, O(poly(n)) size, and $O((\log n)^k)$ depth.

A circuit family contains a different circuit for each length n, with no constraint on the relationship between the circuits. This has some surprising consequences.

Example 5.7. Let *L* be any *unary* language, that is, $L \subseteq \{1\}^*$. For each $n \in \mathbb{N}$, if $1^n \in L$, let C_n be a circuit that always has value 1 (an AND gate with fan-in zero), and if $1^n \notin L$, let C_n be a circuit that has value 0 (an OR gate with fan-in zero). Then, *L* is recognized by a circuit family with O(n) size and O(1) depth, and is therefore in AC⁰, even if it is undecidable.

To prevent such consequences, we impose a *uniformity* restriction, which says that, given n, the circuit C_n must be constructible under some limitation on computational resources, in the following sense.

Definition 5.8 (DLOGTIME uniformity, Barrington et al., 1990). Let $C = (C_n)_{n \in \mathbb{N}}$ be a circuit family, and assume that the nodes of C_n are numbered from 0 to $|C_n| - 1$. We say that *C* is DLOGTIME-*uniform* if there is a (deterministic) Turing machine that runs in logarithmic time and accepts those tuples $\langle f, i, j, 1^n \rangle$ such that in C_n , node *i* has label *f* and there is an edge from node *i* to node *j*.

For the rest of this section, whenever we mention a circuit complexity class, we mean the DLOGTIME-uniform version of it.

Figure 5.2 shows the language classes in the Chomsky hierarchy and the circuit classes we'll use in this course. AC^0 is the smallest circuit complexity class we'll be interested in. As a reminder, the 0 means that the circuit families in this class have bounded (O(1)) depth.

The classic examples of languages not in this class are (Furst et al., 1984):

 $PARITY = \{ \mathbf{w} \in \{0, 1\}^* \mid \mathbf{w} \text{ has an odd number of } 1's \}$ $MAJORITY = \{ \mathbf{w} \in \{0, 1\}^* \mid \mathbf{w} \text{ has more } 1's \text{ than } 0's \}.$

 TC^0 contains both of the above languages, and many more. What are some languages not in TC^0 ? This question is especially interesting, because, as we will

see (Section 8.1), we think such languages would be too difficult for transformers. There is an open question of whether $TC^0 = NC^1$ (analogous to the more famous question of whether P = NP), and it's widely believed that $TC^0 \neq NC^1$. If they are in fact different, then any NC¹-complete language is not in TC^0 .

 One example of an NC¹-complete language is the Boolean Formula Value Problem (BFVP). The instances are propositional formulas built up from constants 0 and 1 and the connectives ∧, ∨, ¬, and the problem is to decide whether such a formula is true or not. In other words, it is defined by the following context-free grammar:

```
S \to F_{1}
F_{1} \to (F_{1} \land F_{1})
| (F_{0} \lor F_{1}) | (F_{1} \lor F_{0}) | (F_{1} \lor F_{1})
| (\neg F_{0})
| 1
F_{0} \to (F_{0} \land F_{0}) | (F_{0} \land F_{1}) | (F_{1} \land F_{0})
| (F_{1} \lor F_{1})
| (\neg F_{1})
| 0
```

Linguistically, the ability to evaluate Boolean formulas is directly relevant to computations underlying compositional semantics.

• The canonical example of a regular but NC¹-complete language is the word problem for S_5 . A permutation of [k] is a bijection $\pi : [k] \rightarrow [k]$, and S_k is the set of all permutations of [k]. Treating S_k as an alphabet and compositions of permutations as strings, we can define the language $W(S_k)$ of compositions of permutations of [k] that equal the identity permutation. For example, in S_5 , the permutation (01) swaps 0 and 1, while the permutation (01234) cycles 0 to 1, 1 to 2, and so on, and 4 to 0. So (01)(01) $\in W(S_5)$ but (01234)(01234) $\notin W(S_5)$. These languages are easy for finite automata to recognize, but difficult with only fixed computation depth.

5.2.3 Overview of results

Where do transformers sit in Fig. 5.2?

• If attention is simplified to *unique-hard* attention, in which attention is entirely focused on one position, then transformers are in AC⁰ (Chapter 7) and therefore cannot recognize PARITY.

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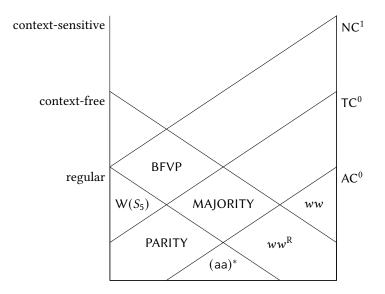


Figure 5.2: Some complexity classes defined by circuit families and logics, compared with the perhaps more familiar Chomsky hierarchy. Assumes $TC^0 \neq NC^1$ and $L \neq NL$. Circuit classes are DLOGTIME-uniform.

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- Otherwise, transformers appear to be in TC⁰ (Section 8.1).
- But if we allow transformers to take *intermediate steps*, they are Turing-complete (Chapter 9).

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