

Quantum Computing Introduction

Peter M. Kogge
Jonathan Baker
Univ. of Notre Dame

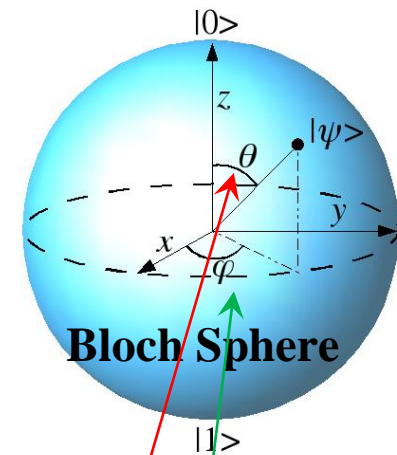
Excellent references:

- <https://homes.cs.washington.edu/~oskin/quantum-notes.pdf>
- <https://arxiv.org/pdf/quant-ph/9809016.pdf>
- <http://www.dwavesys.com/>
- “Quantum Annealing, Uses, Capabilities, and Potential,” M. Thom – D-Wave
- <http://www.research.ibm.com/quantum/>
- “A Quantum Macro Assembler,” Scott Pakin
- <https://www.research.ibm.com/ibm-q/>



Intro

- Classical Computing: bit = “0” or “1”
- Quantum Computing: **qubit**
 - Takes on basis states $|0\rangle$ or $|1\rangle$ only when “measured”
 - Rest of time in a “superposition” of possible states
 $|\psi\rangle = \text{“state of qubit”} = a|0\rangle + b|1\rangle,$
and a, b *complex numbers* and $|a|^2 + |b|^2 = 1$
 - $|a|^2$ is probability of being in $|0\rangle$ state
 - $|b|^2$ is probability of being in $|1\rangle$ state
- Physical phenomena
 - Photon: Vertical or Horizontal polarization
 - Electron: Spin up or Spin down
 - Atom: Discrete energy levels
- Computation: Take a vector $|\psi\rangle$ & “rotate” it.(2 angles)



Computation

- In classical logic, we use gates to manipulate the bits
- To manipulate a qubit, we use “quantum gates”
 - These gates can be represented as matrices
 - These matrices are unitary
 - $(U^T)*U = I$
 - More importantly: **Reversible** (Invertible)
- The logic gates we are used to are **Irreversible**
- This means an operation on a qubit can be “undone”



Computation

- To perform a “quantum algorithm” perform some series of quantum gates
 - Apply matrices to the vector representing the quantum system
- *Evolution of state* in closed quantum system
 - $|\psi_{t1}\rangle = U |\psi_{t0}\rangle$ where $t1$ is some time after $t0$
 - and U some unitary matrix



A Simple Quantum “NOT” Gate

- Traditional logic $\sim 1 = 0$ and $\sim 0 = 1$
- Quantum: “Not” should reverse probabilities
- $\sim(a|0\rangle + b|1\rangle) = b|0\rangle + a|1\rangle$ (Switch coefficients)
- If we write value of a qubit as a vector $\begin{bmatrix} a \\ b \end{bmatrix}$
- Then can write \sim as $\begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix} \begin{bmatrix} a \\ b \end{bmatrix}$
- Thus U for “not” is $\begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix}$



Multi-Qubit Systems

- Given 2 independent qubits

- $|x\rangle = a|0\rangle + b|1\rangle$

- $|y\rangle = c|0\rangle + d|1\rangle$

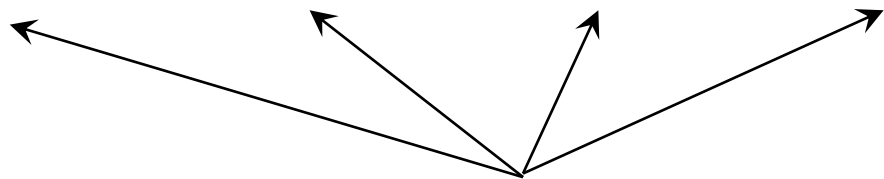
$$|x\rangle = \begin{bmatrix} a \\ b \end{bmatrix}$$

$$|y\rangle = \begin{bmatrix} c \\ d \end{bmatrix}$$

- “State” of composite system is “tensor product” $|x\rangle \otimes |y\rangle$

- $ac|0\rangle|0\rangle + ad|0\rangle|1\rangle + bc|1\rangle|0\rangle + bd|1\rangle|1\rangle$

- $= ac|00\rangle + ad|01\rangle + bc|10\rangle + bd|11\rangle$



4 possible states of 2 bits = each a basis vector

- N independent bits thus have **2^N different basis vectors eqvt to 2^N classical bits**



Quantum Entanglement (EPR)

- Pairs of particles where states cannot be described independently of each other
 - Even when particles separated by great distances
- Joint state space: $|00\rangle$, $|01\rangle$, $|10\rangle$, $|11\rangle$
- Once entangled, cannot separate out to individual spaces
- Any transformation on 1 bit affects state of other




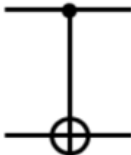


Controlled Not

- Assume entangled $|\psi_1\rangle, |\psi_2\rangle$ in zero state
- Apply $H = \begin{bmatrix} 1 & 1 \\ 1 & -1 \end{bmatrix}$ to one bit = randomize it
 - Thus $|\psi'_1\rangle = (1/\sqrt{2})|0\rangle + (1/\sqrt{2})|1\rangle$
- Apply $C_{\text{not}} = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix}$
- Result = $(1/\sqrt{2})|00\rangle + (1/\sqrt{2})|11\rangle$
- But there is no $|\psi_1\rangle, |\psi_2\rangle$ whose \oplus gives this
 - 2 states cannot be considered independently



Other Quantum Gates

Gate	Notation	Matrix
NOT (Pauli- X)		$\begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix}$
Pauli-Z		$\begin{bmatrix} 1 & 0 \\ 0 & -1 \end{bmatrix}$
Hadamard		$\frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 1 \\ 1 & -1 \end{bmatrix}$
CNOT (Controlled NOT)		$\begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix}$

http://www.mdpi.com/entropy/entropy-16-05290/article_deploy/html/images/entropy-16-05290f1-1024.png



Measuring A Quantum System

- At any given time, we cannot know what state a quantum bit is in
- In order to determine this, we must *measure* the system
 - By doing so, we collapse the system, and we cannot go back to where we left off.
 - Once you measure, the superposition is lost



Quantum Computation

- All known quantum algorithms solve “promise” problems
 - Structure of solution space promised to be of some form
- Use superposition, entanglement, & interference to extract info about structure
- Classically, must compute every point in solution space to obtain full knowledge
- Quantum computes every point using quantum parallelism
- Conceptually provides exponential speedup



Classical Logic on Quantum Computers

$$H = \frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 1 \\ 1 & -1 \end{bmatrix} \quad X = \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix}$$
$$\begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \end{bmatrix}$$

Together: These 3 gates can be used to simulate ALL of classical logic, on a quantum computer.



Classical Logic on Quantum Computers

- AND
- OR ($= \neg \text{AND}$)
- NOT
- etc.
- Every circuit in classic logic can be expressed using these gates
- Universal gates (NAND, NOR, Toffoli)



Satisfiability

- As we've seen in this class k-SAT is not fast on classical computers
- Question: Can we solve this problem faster by considering it on a quantum computer
- Recall that the gates are reversible
 - So if we set the output can we determine the inputs give that output
 - In fact, we can get all possible assignments this way

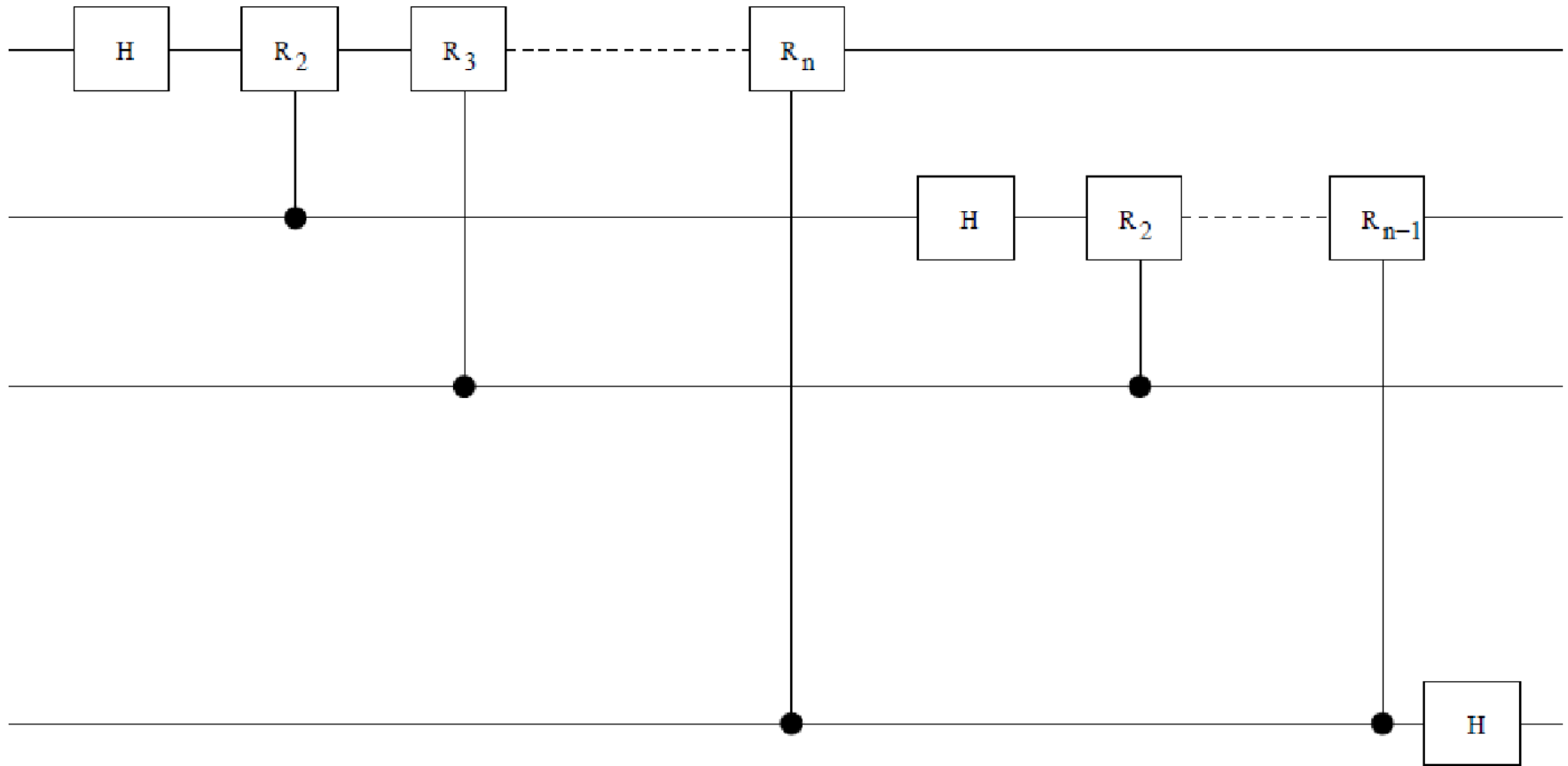


Known Algorithms

- Algorithms based on quantum Fourier transform
 - Deutsch–Jozsa algorithm
 - Simon's algorithm
 - Quantum phase estimation algorithm
 - – **Shor's algorithm: Integer factorization problem** (NP-Hard solved in poly time)
 - Hidden subgroup problem
 - Boson sampling problem
 - Estimating Gauss sums
 - Fourier fishing and Fourier checking
- Algorithms based on amplitude amplification
 - Grover's algorithm
 - Quantum counting
- Algorithms based on quantum walks
 - Element distinctness problem
 - Triangle-finding problem
 - Formula evaluation
 - Group commutativity
- BQP-complete problems
 - Computing knot invariants
 - Quantum simulation



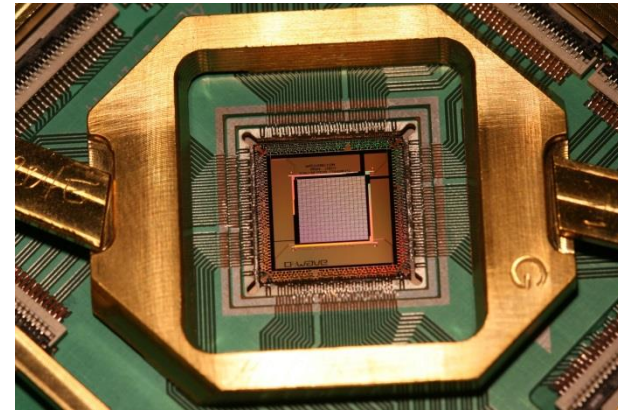
Quantum Fourier Transform



The D-WAVE Quantum Computer



http://www.dwavesys.com/sites/default/files/styles/square_480x480/public/D-Wave%20Two%20in%20Lab.png?itok=PKiVp30g



<http://www.dwavesys.com/sites/default/files/D-Wave%201000Q%20-%20lower%20res1.jpg>



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<http://3.bp.blogspot.com/-cKMKqBBNbw/VmgmsYXuLOI/AAAAAAAAAltY/zMxL5ncod-Y/s1600/google-d-wave-quantum-computer-3.jpg>

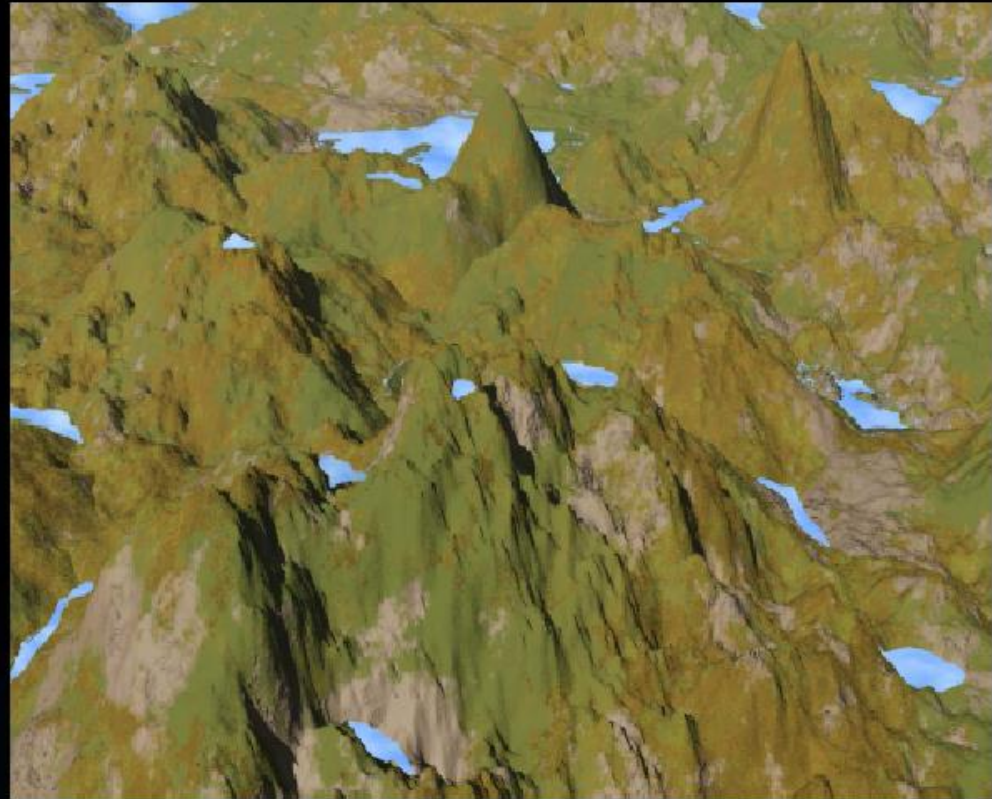


Quantum Annealer Scaling



Energy Landscape

- Space of solutions defines an energy landscape & best solution is **lowest** valley
- Classical algorithms must **walk over** this landscape
- Quantum annealing uses **quantum effects** to go **through** the mountains



Describing a Circuit Quantumly

Listing 2. QASM version of Figure 2 (circcsat.qasm)

```
1 # Solve a circuit-satisfiability problem.
2
3 !include <gates>
4
5 !use_macro not1 not_x4
6 not_x4.$A = x3
7 not_x4.$Y = $x4
8
9 !use_macro or2 or_x5
10 or_x5.$A = x1
11 or_x5.$B = x2
12 or_x5.$Y = $x5
13
14 !use_macro not1 not_x6
15 not_x6.$A = $x4
16 not_x6.$Y = $x6
17
18 !use_macro and3 and_x7
19 and_x7.$A = x1
20 and_x7.$B = x2
21 and_x7.$C = $x4
22 and_x7.$Y = $x7
23
24 !use_macro or2 or_x8
25 or_x8.$A = $x5
26 or_x8.$B = $x6
27 or_x8.$Y = $x8
28
29 !use_macro or2 or_x9
30 or_x9.$A = $x6
31 or_x9.$B = $x7
32 or_x9.$Y = $x9
33
34 !use_macro and3 and_x10
35 and_x10.$A = $x8
36 and_x10.$B = $x9
37 and_x10.$C = $x7
38 and_x10.$Y = x10
```

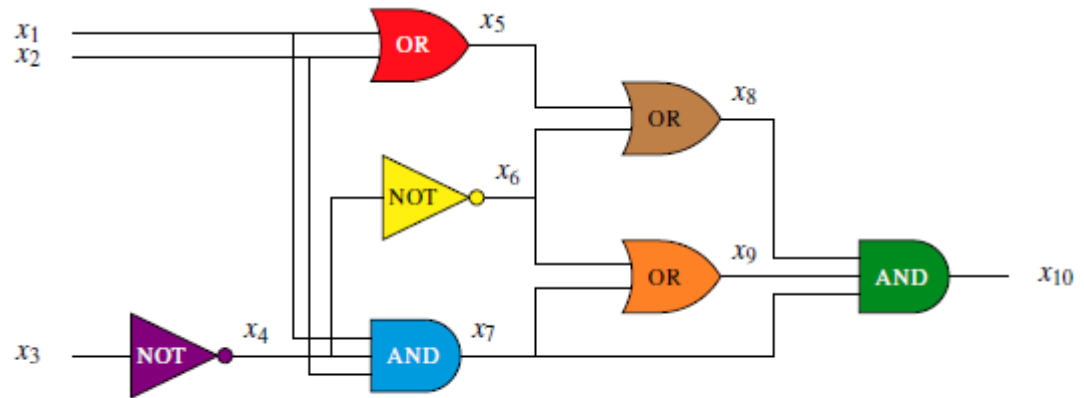


Fig. 2. A sample logic circuit

**What happens if we force x10 to 1?
We solve the corresponding SAT problem!**

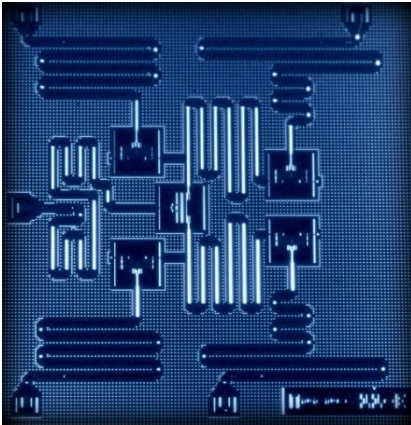
S. Pakin, "A quantum macro assembler," *2016 IEEE High Performance Extreme Computing Conference (HPEC)*, Waltham, MA, USA, 2016, pp. 1-8.

doi: 10.1109/HPEC.2016.7761637

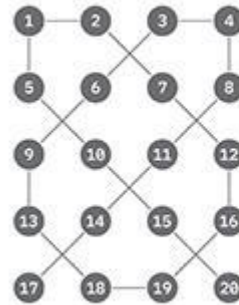
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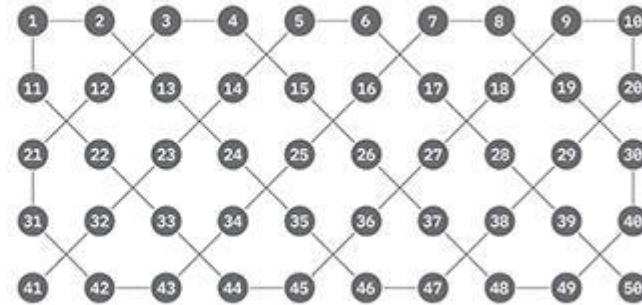
IBM Quantum Computer



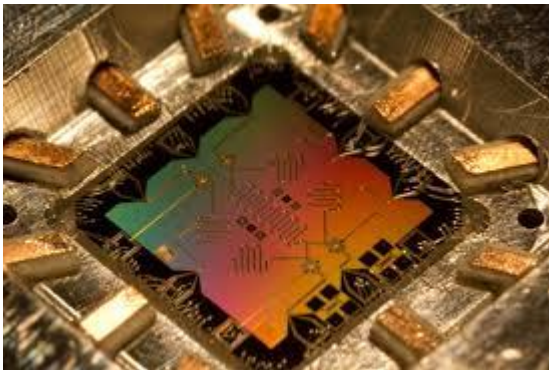
A qubit



Current 20 qubit Computer



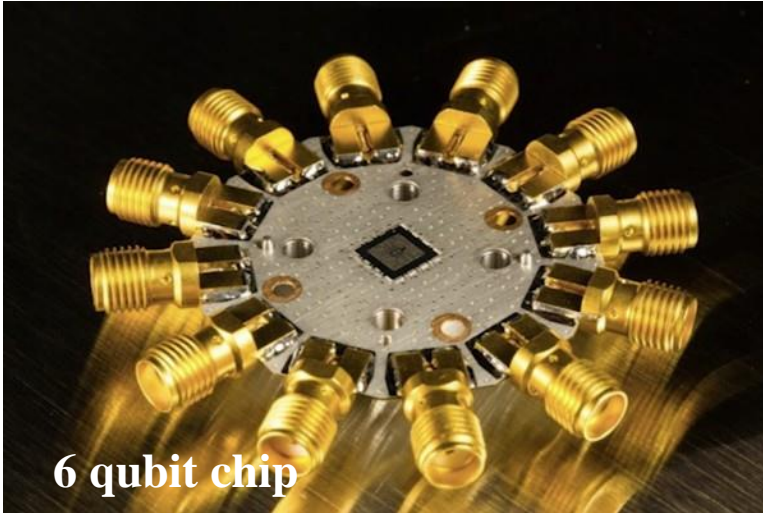
50 qubit chip in test



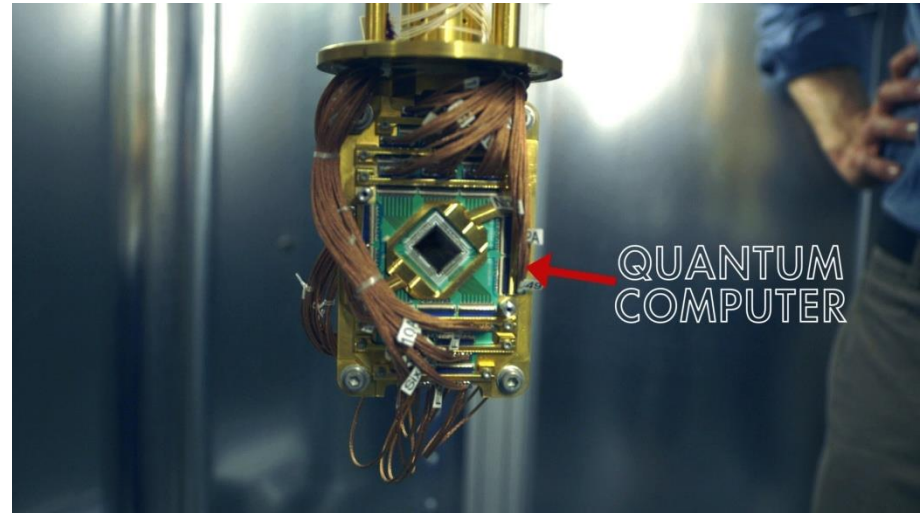
<https://www.research.ibm.com/ibm-q/>



Google Quantum Computer



<https://cdn.technologyreview.com/i/images/chip2.jpg?sw=600&cx=0&cy=0&cw=1363&ch=912>



<http://fossbytes.com/wp-content/uploads/2014/09/maxresdefault.jpg>

